

Serial No: 10/726,302
Examiner: J. Zwelzig
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

REMARKS/ARGUMENTS

Reconsideration is requested in view of the following remarks. Claims 2-5 and 9 are allowed. Claim 6 has been canceled. Claim 7 has been amended. Claims 2-5 and 7-9 remain pending in the application.

Claim Rejections – 35 USC §102

Claim 6 is rejected under 35 U.S.C. §102(b) as anticipated by JP2001153924. Claim 6 has been canceled, rendering this rejection moot. Applicant does not concede the correctness of the rejections.

Favorable reconsideration in the form of a Notice of Allowance is requested. If the Examiner believes a telephone conference would advance the prosecution of this application, the Examiner is invited to telephone the undersigned at the below-listed telephone number.



Dated: Nov 21, 2005

Respectfully submitted,

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